

WHAT IS CLAIMED IS

1. A semiconductor integrated circuit,  
5 comprising:  
    a shielded wire line; and  
    a shielding wire line provided for the  
shielded wire line and having a width broader than  
that of the shielded wire line.

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2. A semiconductor integrated circuit,  
15 comprising:  
    a shielded wire line; and  
    a plurality of shielding wire lines  
provided for the shielded wire line on one side of  
the shielded wire line.

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3. A semiconductor integrated circuit,  
25 comprising:  
    a shielded wire line; and  
    a shielding wire line provided along only  
a portion of an entire extent of the shielded wire  
line.

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4. The semiconductor integrated circuit  
35 as claimed in claim 3, further comprising a driver  
that transmits a signal to the shielded wire line,  
wherein the portion of the entire extent of the

shielded wire line along which the shielding wire line is provided is a portion on a side of the driver.

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5. A method of determining wire lines of a semiconductor integrated circuit, comprising the steps of:

- 10 providing as a library, shielding effects of partial shielding that shields only a portion of an entire extent of a shielded wire line;
- determining a length of the shielded wire line;
- 15 determining a desired shielding effect;
- and
- determining a length of a shielding wire line by looking up the length of the shielded wire line and the desired shielding effect in the
- 20 library.

- 25 6. The method as claimed in claim 5,  
wherein the step of providing as a library the  
shielding effects of partial shielding provides, as  
the library, information about signal delay time  
that appears when a portion of the entire extent of
- 30 the shielded wire line is shielded.